

WHAT IS CLAIMED IS:

1. A functional block comprising:

a system bus synchronizing with a system clock;

5 a functional circuit synchronizing with a local
clock; and

a synchronizing circuit for determining the
operation timing for signal exchange between said
system bus and said functional circuit, wherein

10 said synchronizing circuit takes in said system
clock and said local clock and determines said
operation timing by determining the access time of said
functional circuit from said system clock.

2. The functional block according to claim 1,
wherein said synchronizing circuit takes in said system
15 clock and said local clock and determines said
operation timing by creating an access end signal for
said functional circuit from said system clock.

3. The functional block according to claim 2,
wherein said access end signal is created by using a
20 counter actuated in synchronization with said system
clock and a comparing circuit to compare the output of
said counter with a frequency specifying signal.

4. The functional block according to claim 1,
wherein said synchronizing circuit determines the
25 operation timing for signal exchange between said
system bus and said functional circuit, regardless of
the type of said system bus.

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said functional circuit and said synchronizing circuit are integrated into a functional block.

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19. The semiconductor integrated circuit according to claim 17, wherein the frequency of said system clock is not fixed to a specific frequency.

20. The semiconductor integrated circuit according to claim 19, wherein the frequency of said system clock is not fixed to a specific frequency as a result of the input of a frequency specifying signal to said synchronizing circuit.

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